Just-Ahead-Of-Time Controller Recovery

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Introduction

Motivating Scenario: Recent major attacks on the electric grid necessitate domain-specific formal security monitoring solutions for cyber-physical system operations. Detecting unsafe states aids mitigation measures, but preventing unsafe states provides more beneficial and significant impact for recovery.

Just-Ahead-of-Time Controller Recovery

- Parallel, on-the-fly model checking using symbolic execution for pruning unreachable states to determine unsafe states before execution on PLC

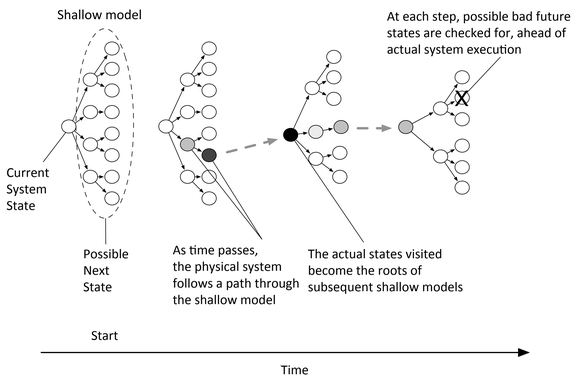


Figure 1: Discarding unreachable states

Controller Logic Modified Attack

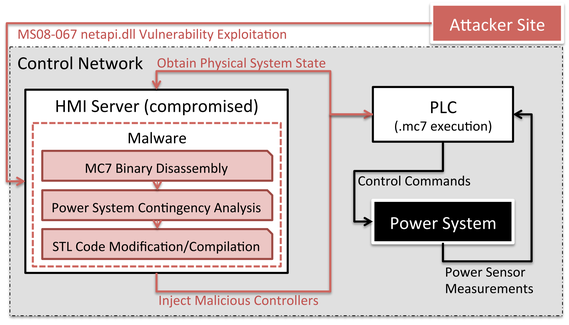


Figure 2: Controller logic modified attack

1. Exploits MS08-067 vulnerability in netapi.dll
2. Injects malicious instructions to the running PLC dynamically
3. The malware copies the dynamic memory, disassembles, injects malicious instruction, assembles, and then uploads it back into the PLC
4. JCR was successful against this attack

JCR Architecture

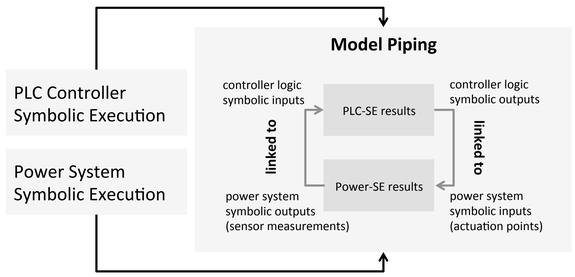


Figure 3: Hybrid Cyber-Physical Symbolic Execution

- JCR uses hybrid symbolic execution to eliminate the unreachable states, thus increasing the speed of verification
- JCR performs parallel, on-the-fly model checking and informs the operator well in advance about the future unsafe states
- With this in-advance warning, the operator can take necessary actions to prevent the unsafe state

Verification and Recovery

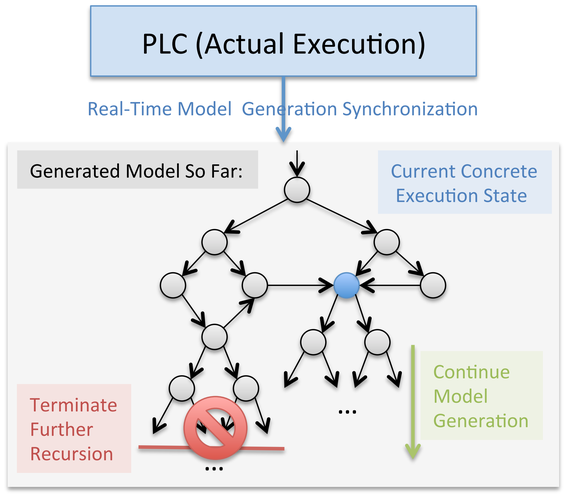
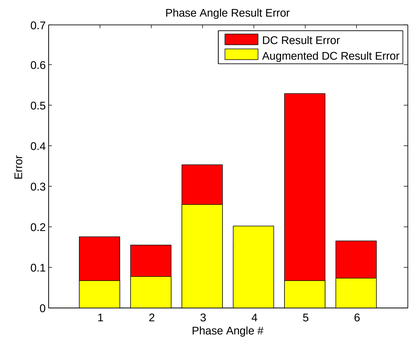


Figure 4: Model Generation, Refinement and Checking

- To address subsequent scan cycles, JCR explores the possible states by creating the corresponding state-based finite state automaton
- JCR avoids exploration of the states that are not reachable from the system's current concrete state

Physical System Symbolic Analysis

- JCR enhances the traditional numerical state estimation algorithms for a symbolic execution (analysis) of the power system
- An augmented DC power flow analysis method was developed that, with the inclusion of symbolic variables, maintained speed and accuracy



Evaluations

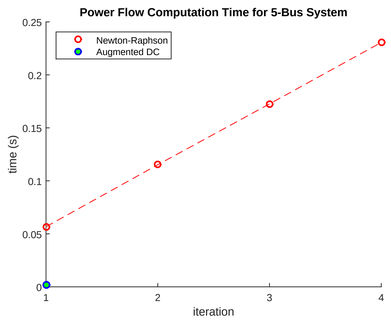


Figure 5: The computation time for the NR-PF method involves many iterations, increasing the total time, whereas the augDC-PF algorithm requires only one iteration.

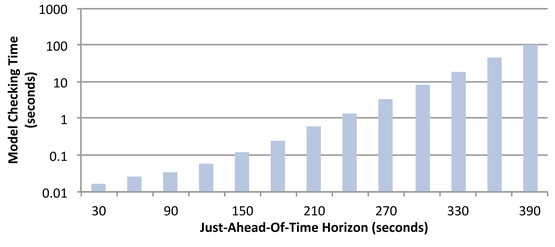


Figure 6: JCR on 2700-bus

References

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