

Work-in-Progress CFPs -11th IEEE Symp. on Industrial Embedded Systems

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CALL FOR WORK-IN-PROGRESS PAPERS

[SIES 2016 11th IEEE International Symposium on Industrial Embedded Systems \(SIES\)](#)

Sponsored by: IEEE Council on Electronic Design Automation (IEEE CEDA) AGH University of Science & Technology in Krakow Intel Poland

AGH University of Science & Technology, Krakow, Poland | 23-25 May 2016 | <http://sies2016.org/>

**** Work-in-Progress Papers - Submission Deadline : April 3, 2016 ****

Keynotes:

- Giulio Corradi, Xilinx, Munich, Germany
- Alberto Sangiovanni-Vincentelli, UC Berkeley, USA
- Wang Yi, Uppsala University, Sweden

SPECIAL SESSIONS

Work-in-Progress papers are invited to be also submitted to Special Sessions

- Mixed-criticality Systems
- Embedded Systems Certification
- Application of Formal Techniques in Embedded System Design and Verification
- Bio-Inspired Embedded Systems in Industry

Topics include, but are not limited to:

- EMBEDDED SYSTEMS
- SYSTEM-on-CHIP and NETWORK-on-CHIP DESIGN & TESTING
- NETWORKED EMBEDDED SYSTEMS
- EMBEDDED APPLICATIONS

FOR TRACKS DETAILS AND SUBMISSION INSTRUCTIONS SEE THE
CONFERENCE WEB SITE

CONTACT SIES 2016 Conference Secretariat: phone: +48-12-617-3034 ; fax:
+48-12-633-2398 ; contact@sies2016.org

[? ASSURE 2016 Call for Papers - 4th International Workshop on Assurance Cases for
Software-intensive Systems Call for Papers: ICCD 2016 in Phoenix, AZ ?](#)



[Call for Papers Embedded Software Systems Engineering Validation and Verification CPS Technologies Foundations](#)
