

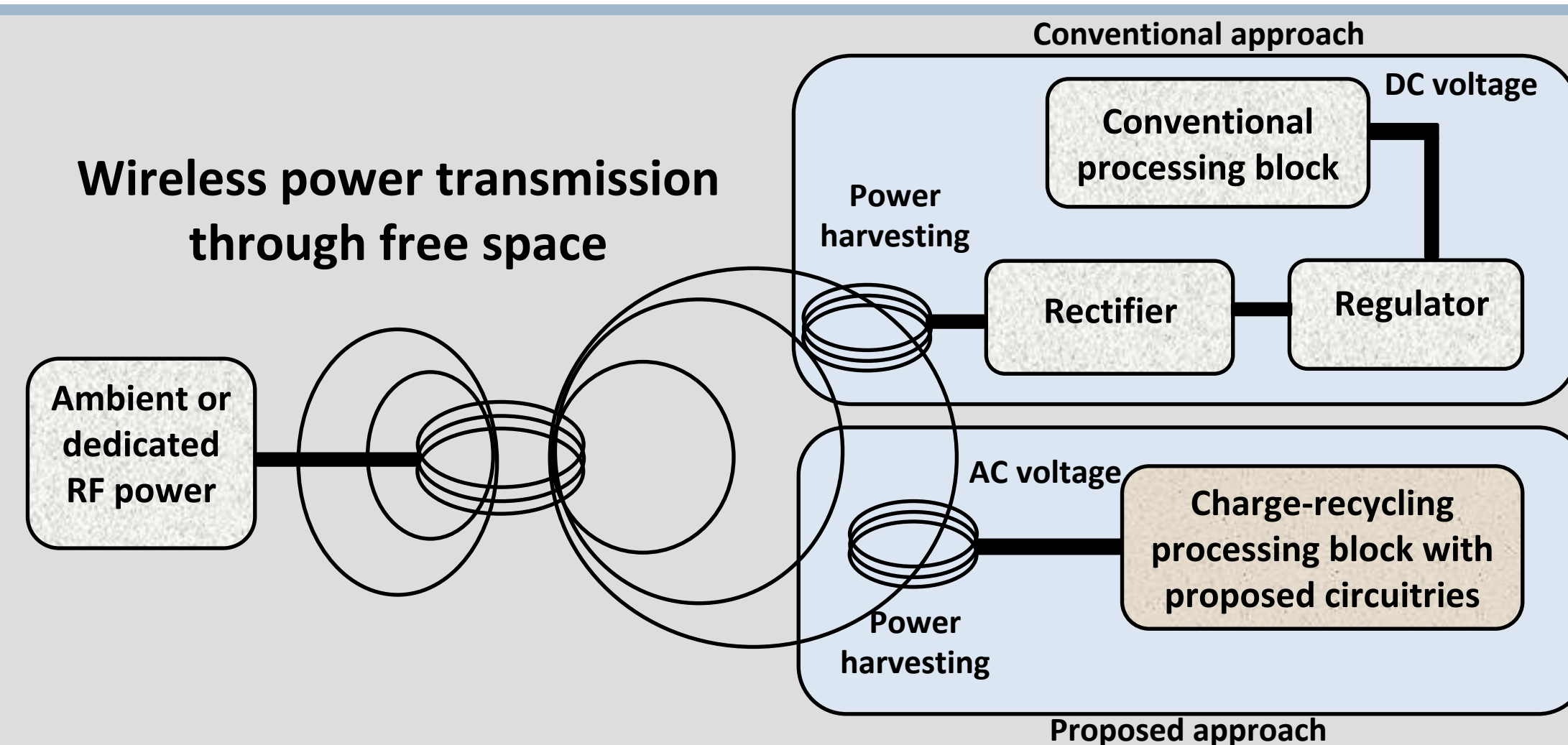
AC Computing Methodology for RF-Powered IoT Devices

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Electrical and Computer Engineering

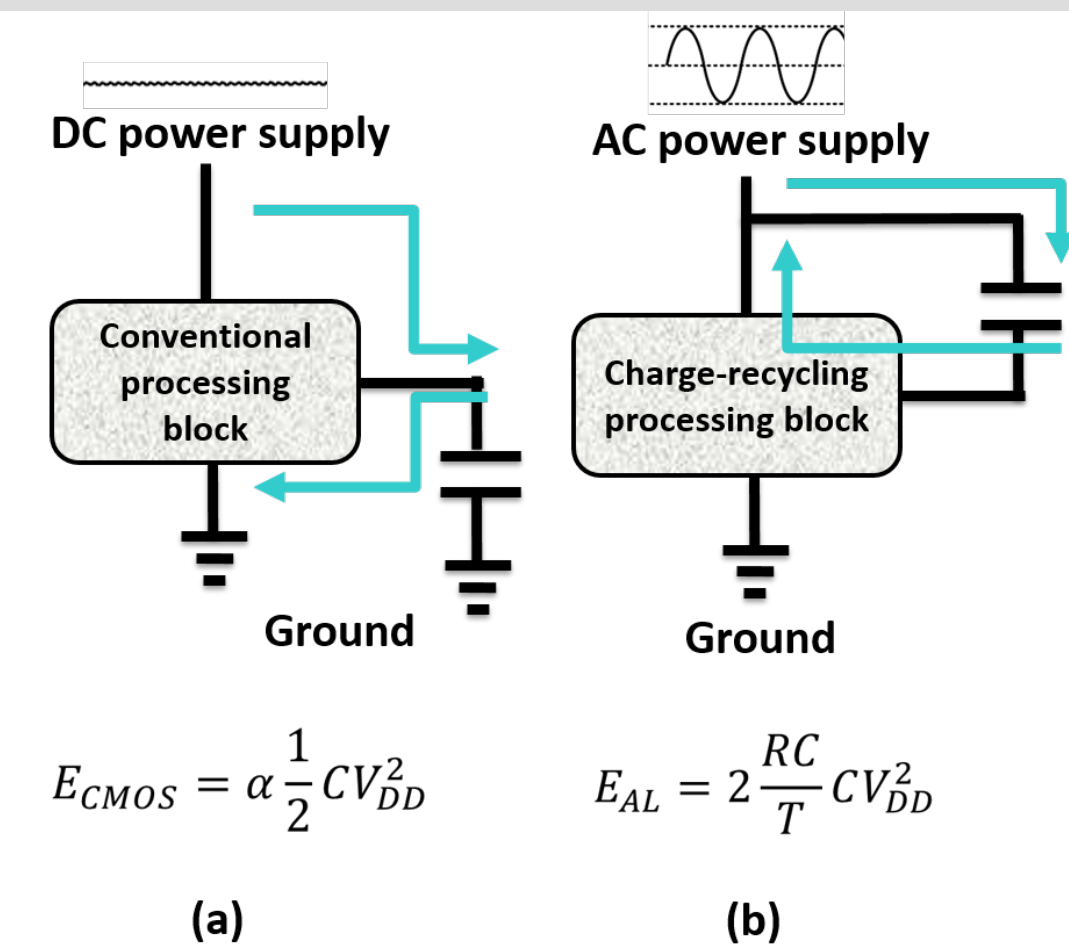
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<https://nanocas.ece.stonybrook.edu>

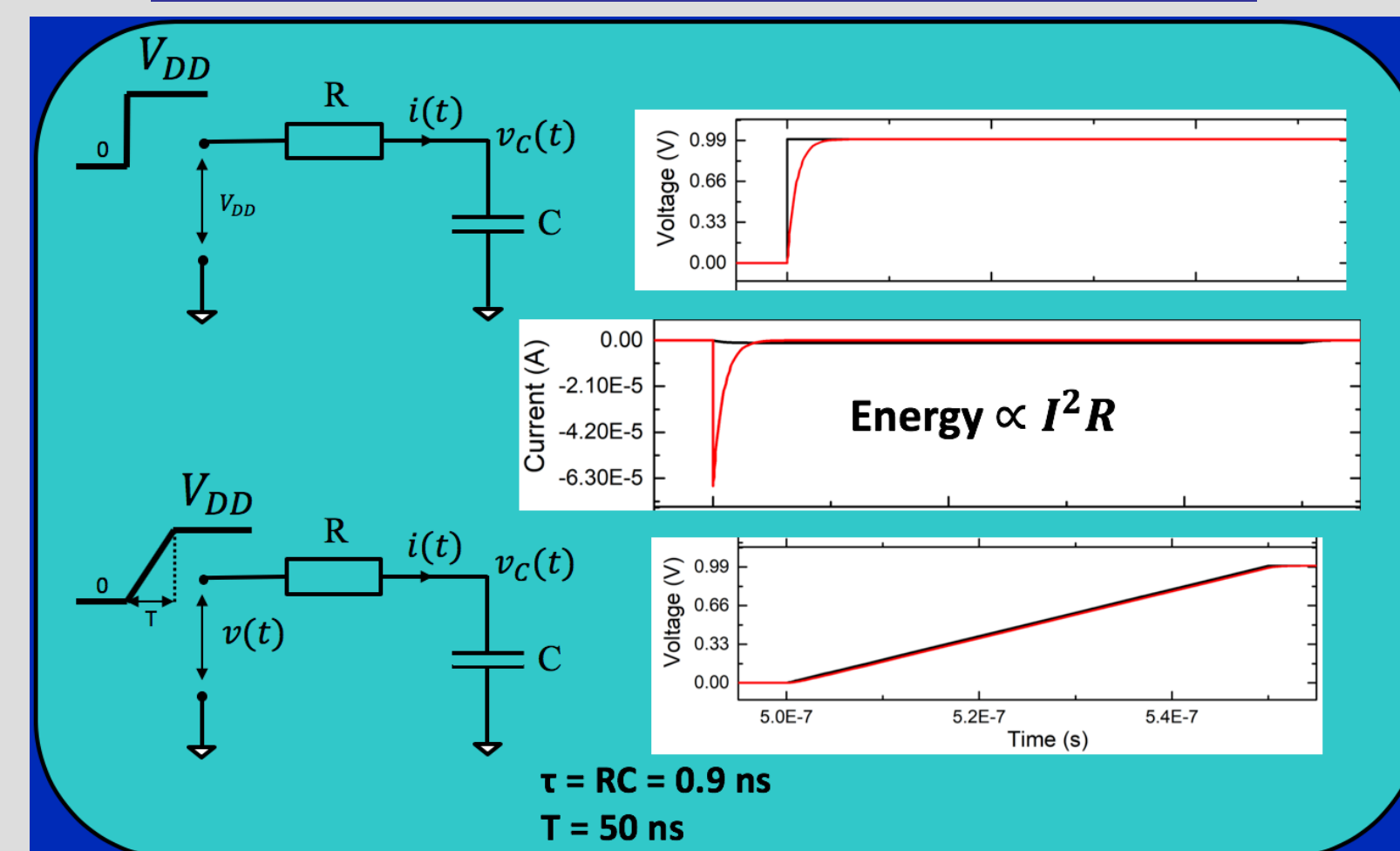


- Energy autonomy is a critical challenge for IoT and WSN based devices
- Ambient and/or dedicated wireless power is a promising energy source
- Leverage AC computing methodology to directly power local processing block
 - More than an order of magnitude increase in energy efficiency
 - Powerful local processing capability
 - Elimination of the power losses due to rectification and regulation
 - Elimination of the strong dependence on battery

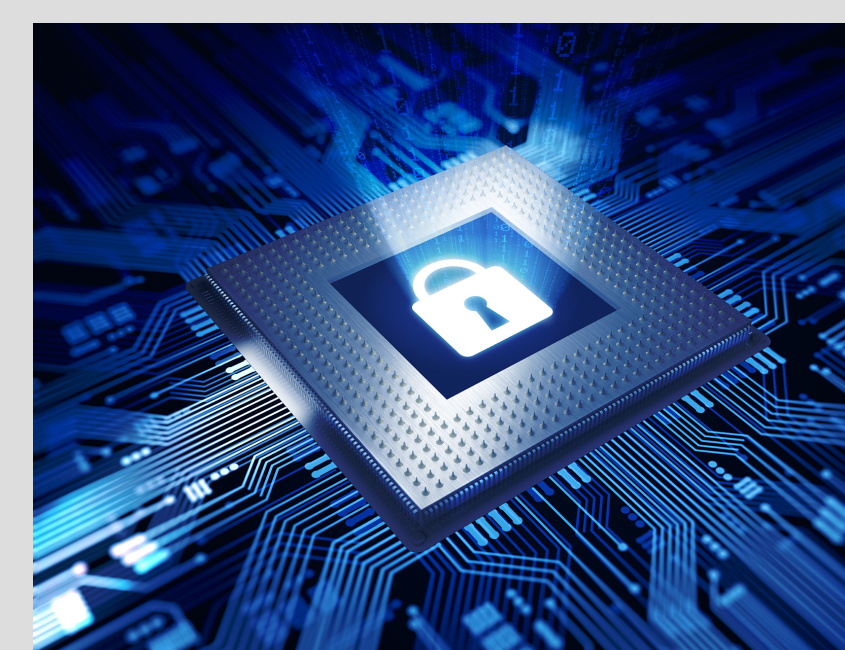
Charge-Recycling Principle



Adiabatic Switching



Potential Applications



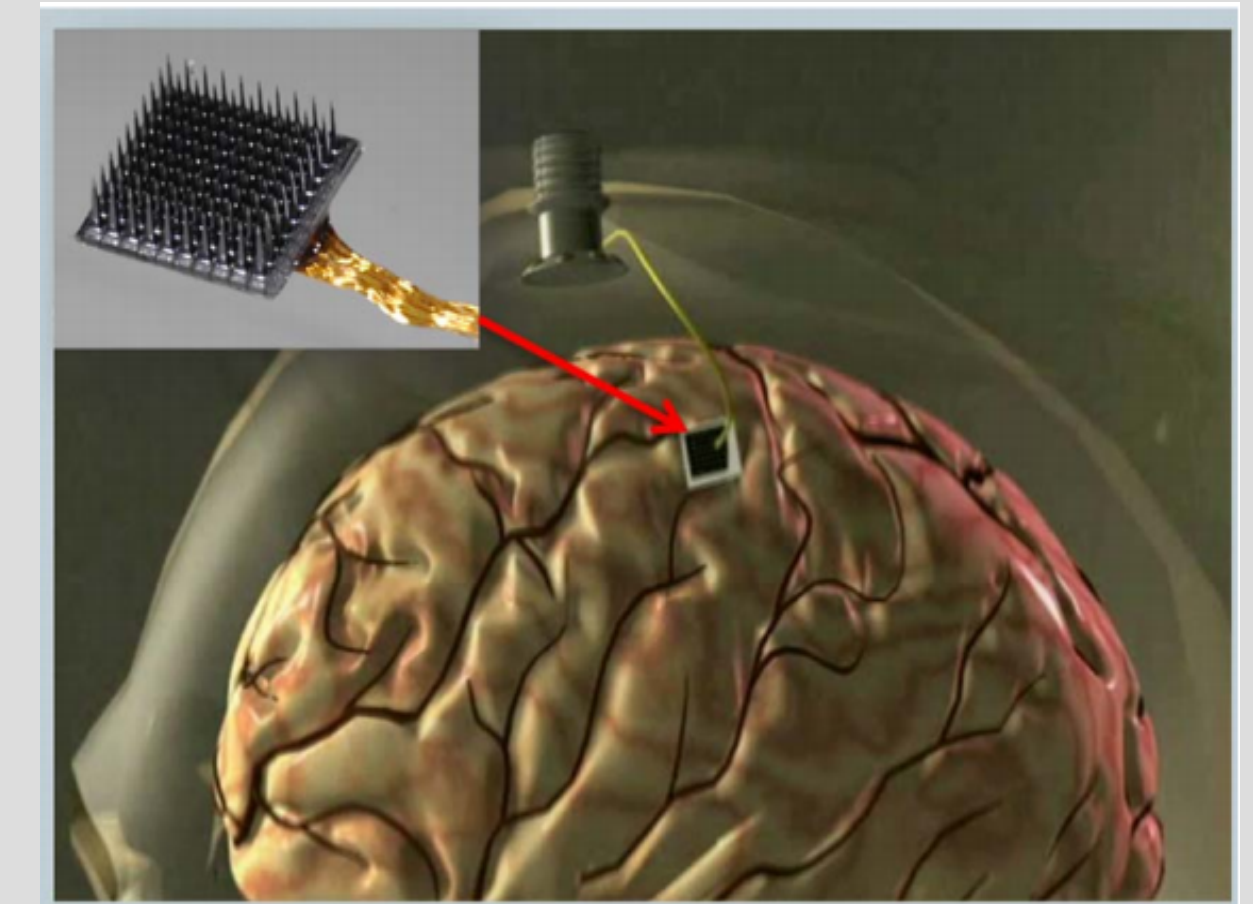
IoT Security



Structural health monitoring



Source: RFID, by AB&R



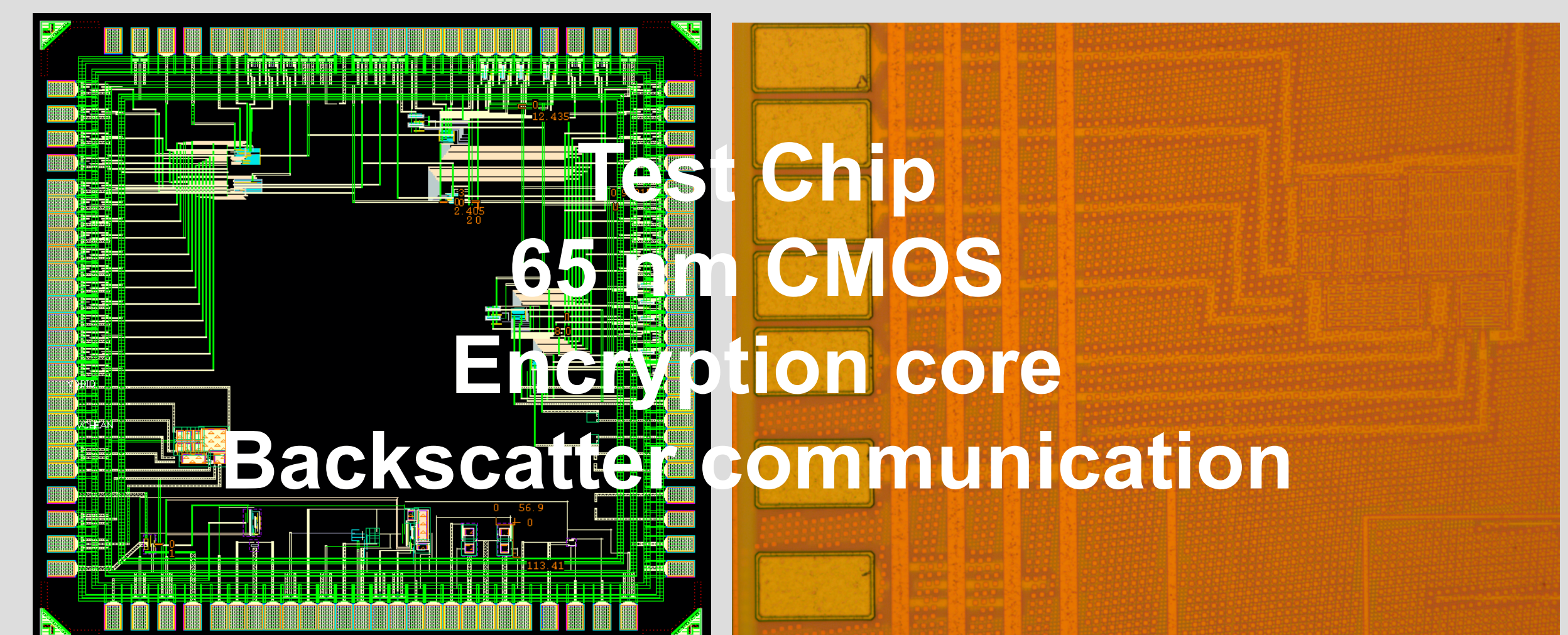
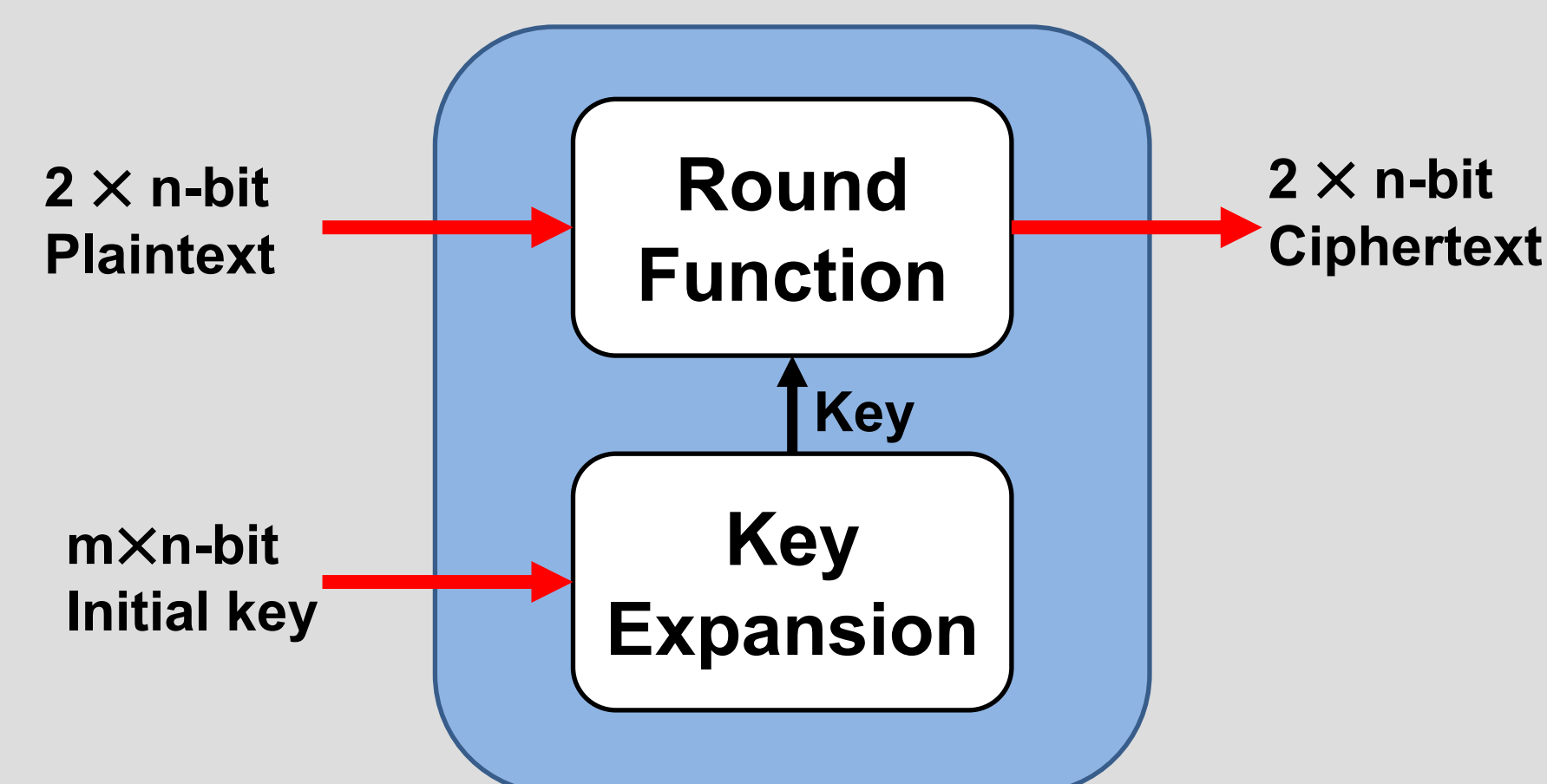
Source: A. V. Nurmikko *et al.*, Proceedings of the IEEE, 2010

SIMON Cipher

- Bit-serialized SIMON32/64
 - Lightweight cryptographic algorithm
 - 32-bit plaintext input
 - 64-bit initial keys
 - Consists of round function and key expansion

Proposed Architecture

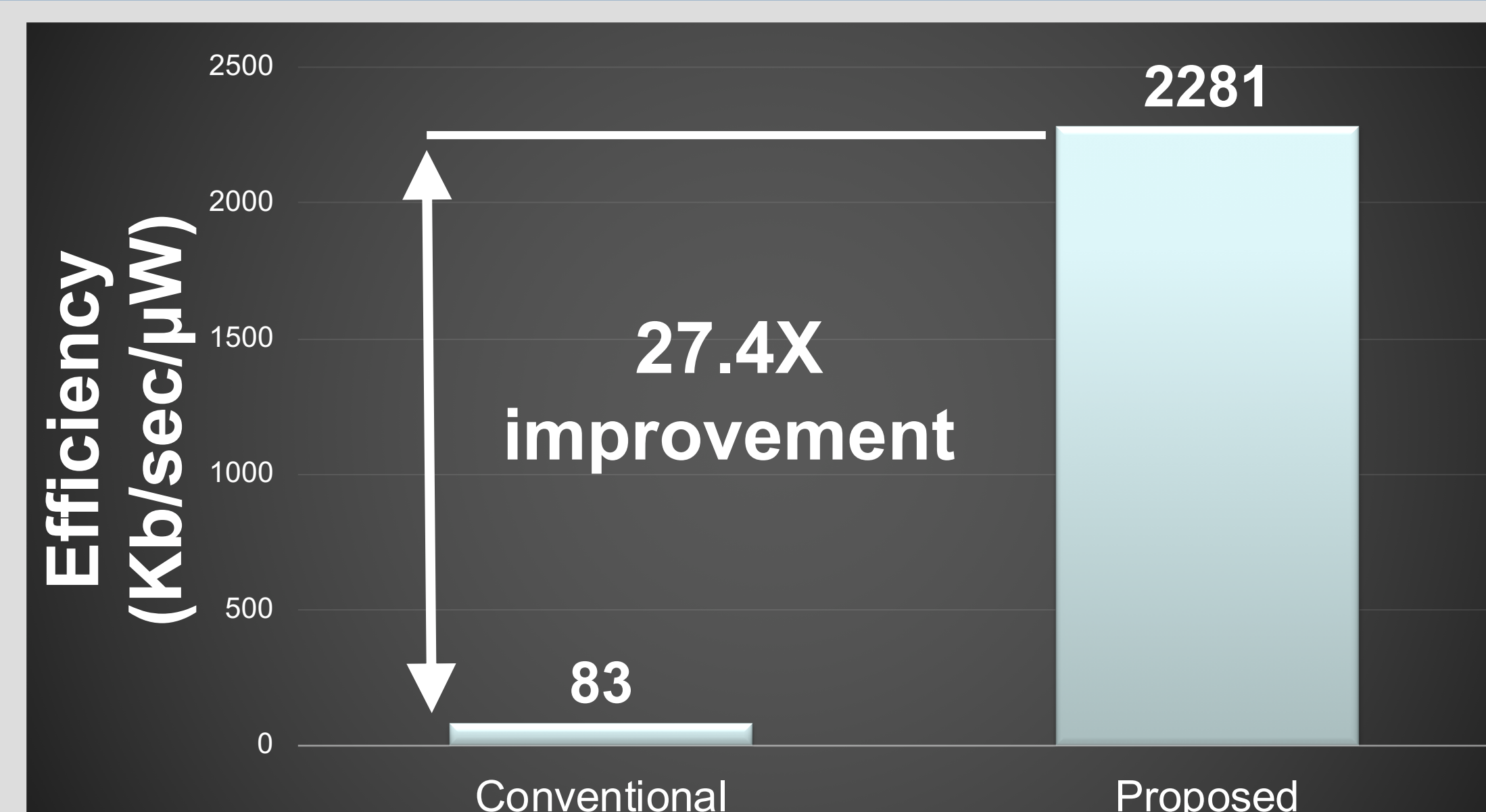
- Primary contributions include
 - Use of adiabatic registers
 - Merging multiplexers and FIFOs to ensure correct synchronization
 - Elimination of the additional flip-flops for appending bits
 - Introduction of balanced transfer paths



Results

- RF signal amplitude is 1.2 V
- Operation frequency is 13.56 MHz
- 65 nm commercial CMOS technology

Architecture	Conventional	Proposed
Logic	Static Logic	PAL
Average Power (μW)	9.12	0.27
Latency (clock cycles)	576	704
Energy (pJ)	387	14
Throughput (Kbps)	753	616
Efficiency (Kb/sec/μW)	83	2281
Number of Transistors	2966	1242



Current Focus

- Power splitting into AC and DC paths
- Integration of a communication framework (backscatter based)
- Further characterization and testing

Publications

- T. Wan, Y. Karimi, M. Stanacevic, and E. Salman, "AC Computing Methodology for RF-Powered IoT Devices," *IEEE TVLSI*, May 2019
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- T. Wan and E. Salman, "Ultra Low Power SIMON Core for Lightweight Encryption," *IEEE ISCAS*, May 2018
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Acknowledgements

This research is supported by

- NSF under grant number 1646318
- Simons Foundation

