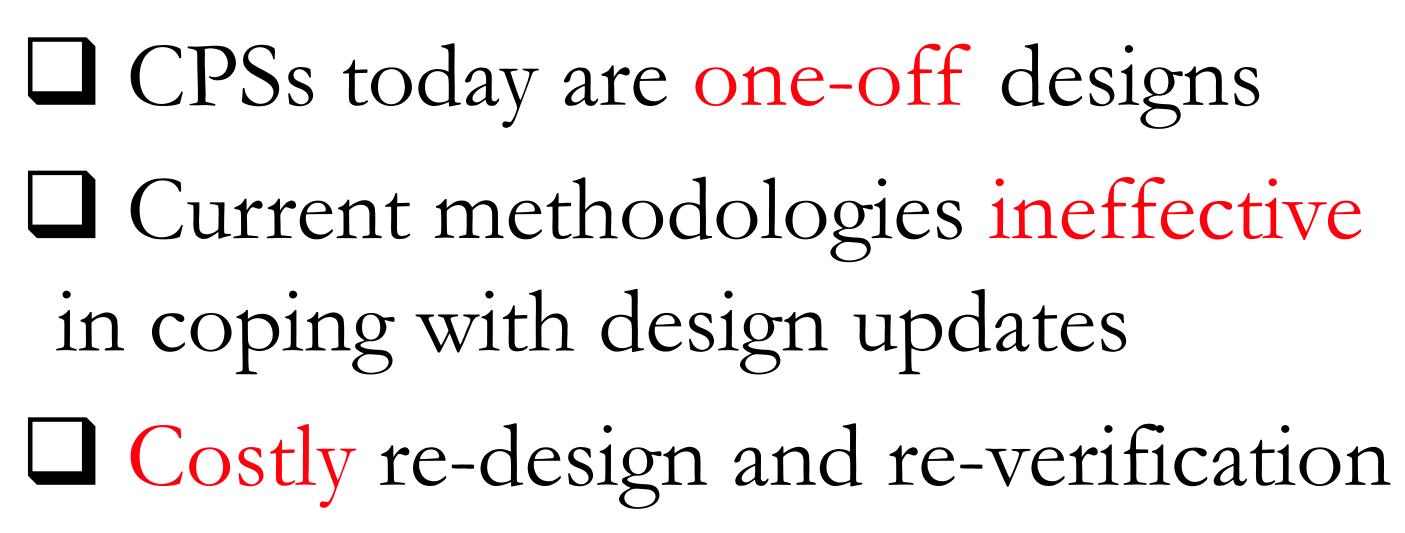
# A Framework for Extensibility-Driven Design of Cyber-Physical Systems CPS: Breakthrough: Collaborative Research: CCF-1646497 & CCF-1646381 Wenchao Li (Boston University), Qi Zhu (UC Riverside), September 1, 2016 – August 31, 2019

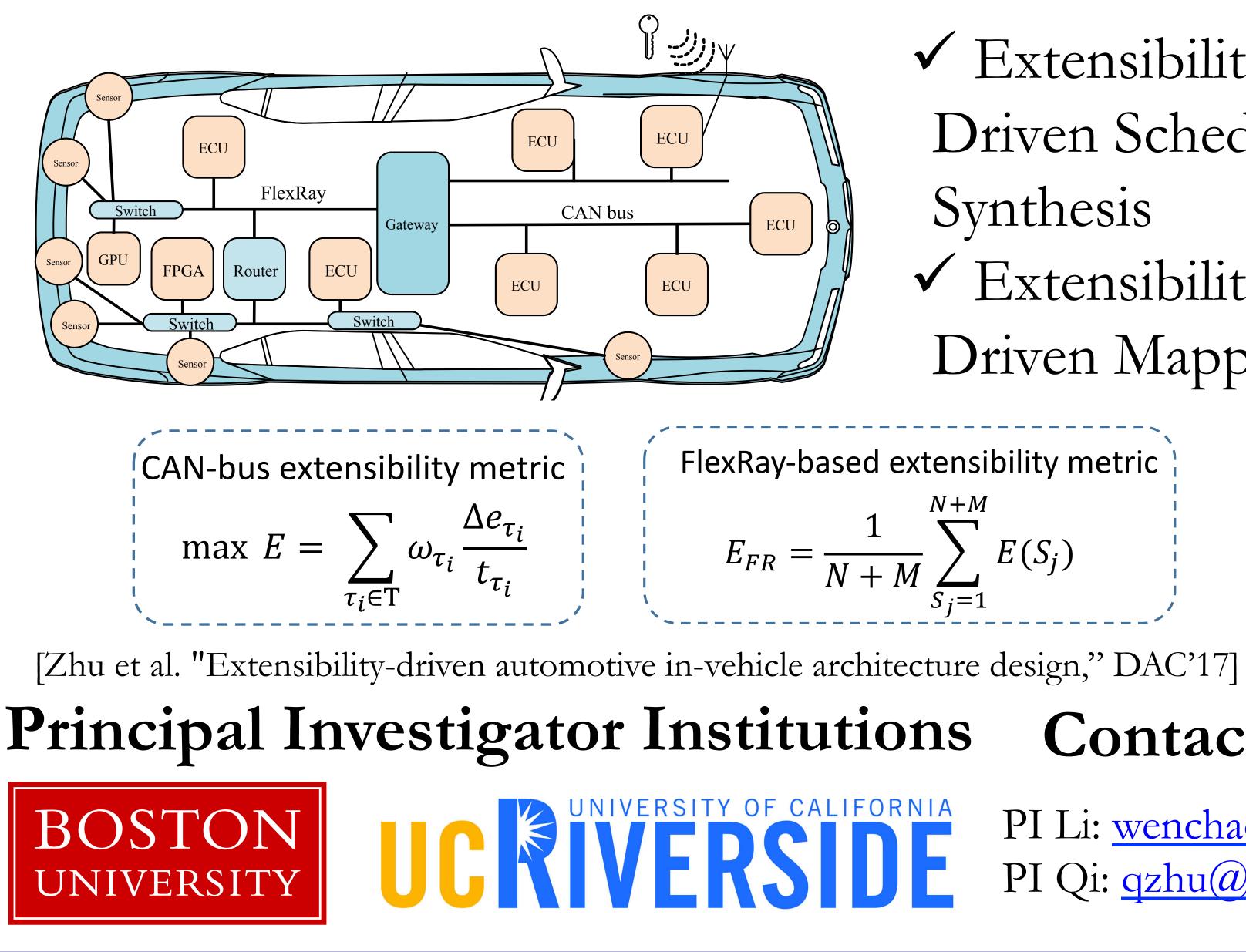
## Challenges





discontinued AMD Am2950 microprocessor.

## **Extensibility for Intra-Vehicle Communication**



# Technical Approach

Extensibility metrics across design layers and update scenarios Priced Time Automata (PTA) to tie together timing, resource usage and functionality in CPS Extensibility-driven software architecture synthesis Verification of time-dependent and SUMO resource-dependent properties TraCl API

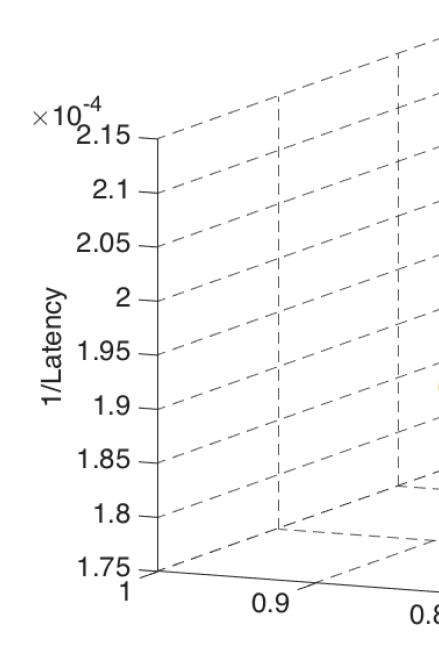
✓ Extensibility-Driven Schedule Synthesis Extensibility-Driven Mapping

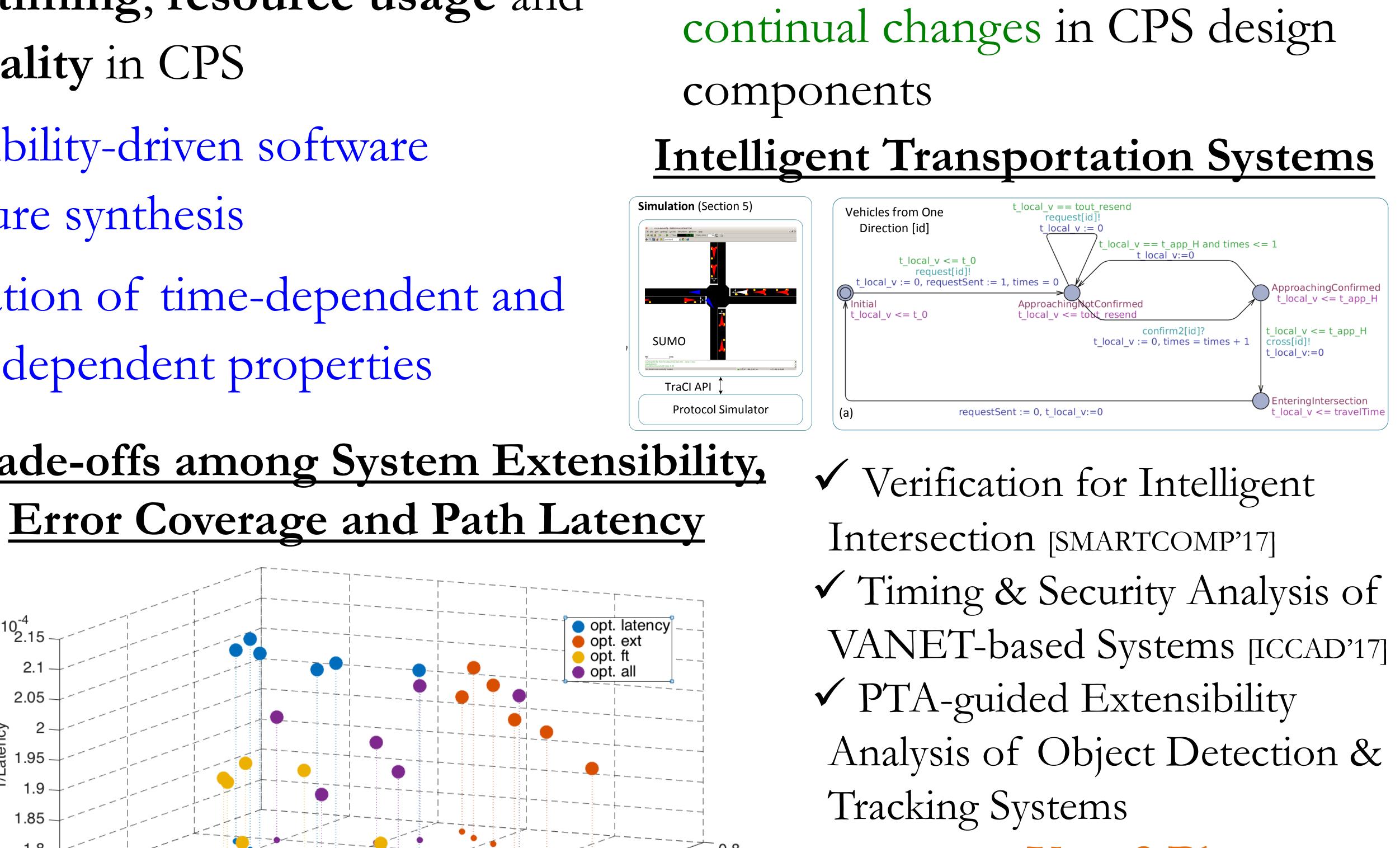
FlexRay-based extensibility metric  $E_{FR} = \frac{1}{N+M} \sum_{S_{i-1}}^{N+M} E(S_j)$ 

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Trade-offs among System Extensibility,





[Liang et al. "Addressing Extensibility and Fault Tolerance in CAN-based Automotive Systems," NOCS'17]



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### Impacts

 Extensibility-driven design flow ✓ New Framework for integrating synthesis and verification

✓ Enable engineers to cope with

### Year 2 Plan

