

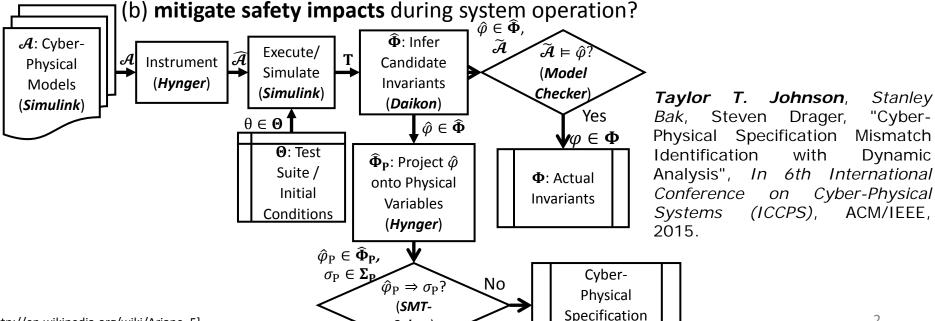
## CRII: CPS: Safe Cyber-Physical Systems Upgrades

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- NSF Awards: 1713253 (Vanderbilt), 1464311 (UT-Arlington)

## **Description**

- *Upgrades* of cyber-physical systems (CPS) may involve (a) cyber components or (b) physical components
- Design <u>reuse</u> common in CPS industries: e.g., Ariane 5 reused inertial measurement unit (IMU) from Ariane 4, motor vehicles across model years and models, etc.
- **Physical system information may be encoded in software**, particularly with regards to control (e.g., parameters)
- Upgrades in CPS may yield *cyber-physical specification mismatches*
- Can we (a) **detect mismatches** automatically at design-time and runtime and

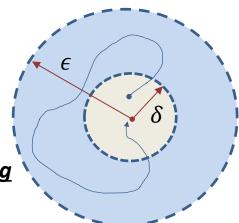
Solver)



Mismatches

## **Findings**

- Demonstrated feasibility to infer CPS specifications from black-box models through <u>specification inference & mining</u> (e.g., Daikon, s-Taliro, Breach, ...)
- Automatically detect more and/or less restrictive specifications, indicating mismatches using satisfiability modulo theories (SMT) solvers
- Overall specification inference and mismatch detection with <u>Simplex</u> <u>architecture</u> to avoid safety violations at runtime
- Most impactful fundamental results are in defining generalized specifications for CPS using <u>hyperproperties for signal temporal</u> <u>logic (HyperSTL)</u>, which generalize properties (sets of traces) to sets of properties (sets of sets of traces) over realtime, real-valued signals
- Lyapunov stability is a hyperproperty



Luan Viet Nguyen, James Kapinski, Xiaoqing Jin, Jyotirmoy Deshmukh, *Taylor T. Johnson*, "Hyperproperties of real-valued signals", *In 15th ACM-IEEE International Conference on Formal Methods and Models for System Design (MEMOCODE)*, IEEE, 2017.

- Specify over space of parameters and execution traces
- Cannot check Lyapunov stability with individual traces, so it is not a trace property
- Mapping parameters  $\delta$ ,  $\epsilon$  to constant signals
- A trace  $\mathbf{w} = \{w_{\delta}, w_{\epsilon}, w_{out}\}$

$$\phi_{Ly} = \{ W \in P \mid \forall \mathbf{w} \in W : \exists \mathbf{w}' \in W : \forall \mathbf{w}'' \in W : w''_{out}(0) < w'_{\delta}(0) \Rightarrow (t > 0 \land w''_{out}(t) < w_{\epsilon}(t)) \}$$

<u>Hynger</u> (hybrid invariant generator) software tool: <a href="https://bitbucket.org/verivital/hynger">https://bitbucket.org/verivital/hynger</a>