CPS Medium: Collaborative Research: CyberMech, a Novel Run-Time Substrate for Cyber-Mechanical Systems

Arun Prakash, Shirley Dyke {aprakas, sdyke}@purdue.edu Purdue University (Grant #: 1136075)

Project Dates: September 2011 – August 2017

Christopher Gill, Kunal Agrawal, Chenyang Lu, {cdgill, kunal, lu}@wustl.edu Washington University in St. Louis (Grant #: 1136073)

Introduction

- > Hybrid Testing: Physical components & numerical model
- Real-Time Hybrid Simulation of structural components

Mumerical Substructure EOM Test Coordination & Control Physical Elements

Challenges

- Complex interaction b/w sensors, actuators, controllers & models etc.
- > Configurable, adaptive concurrency platform for parallel execution
- > Real-times constraints: Multiple time-scale dynamics
- > Asynchronous, on-the-fly adjustments to data flow and control flow

> Co-design of physical components, control algorithms, numerical models and the computational platform they run on

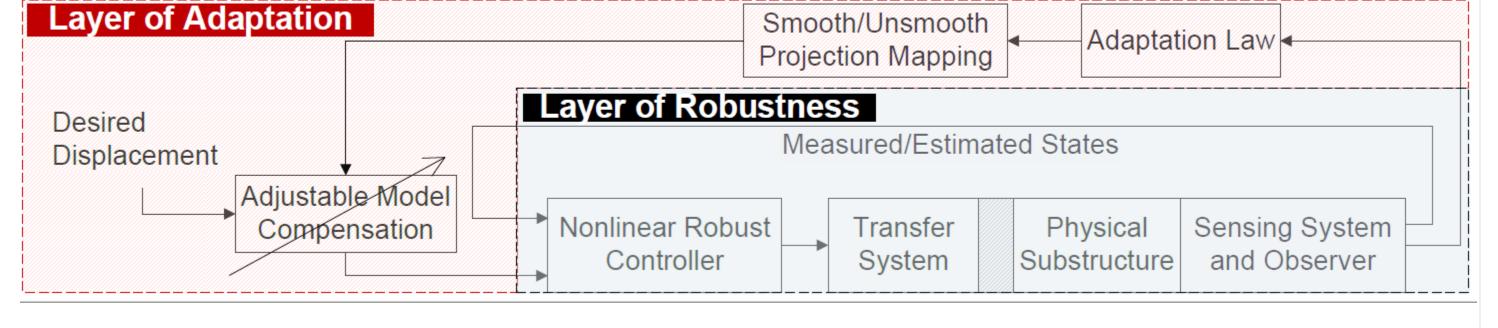
Target Application Domains

- > RTHS with multi-scale models
- Tele-operation and interactive control of cyber-mechanical systems

Nonlinear Self-tuning Robust Control System (SRCSys)

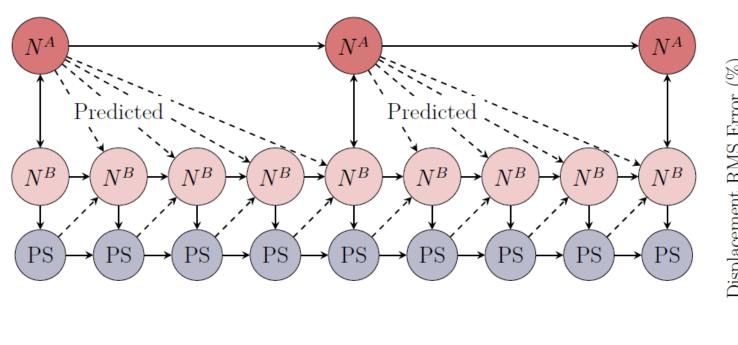
A multi-layer nonlinear control system is designed to accommodate extensive performance variations in the physical sub. due to structural failure, complexity, and nonstationary behavior.

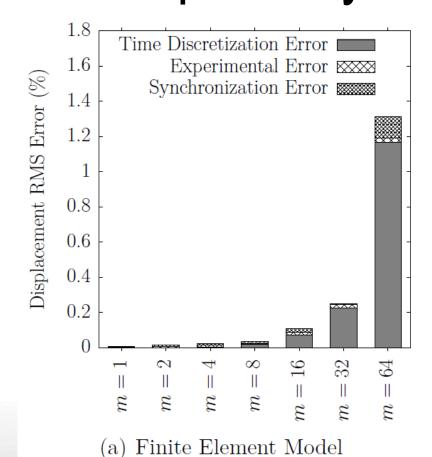
Multi-layer Self-tuning Robust Control System

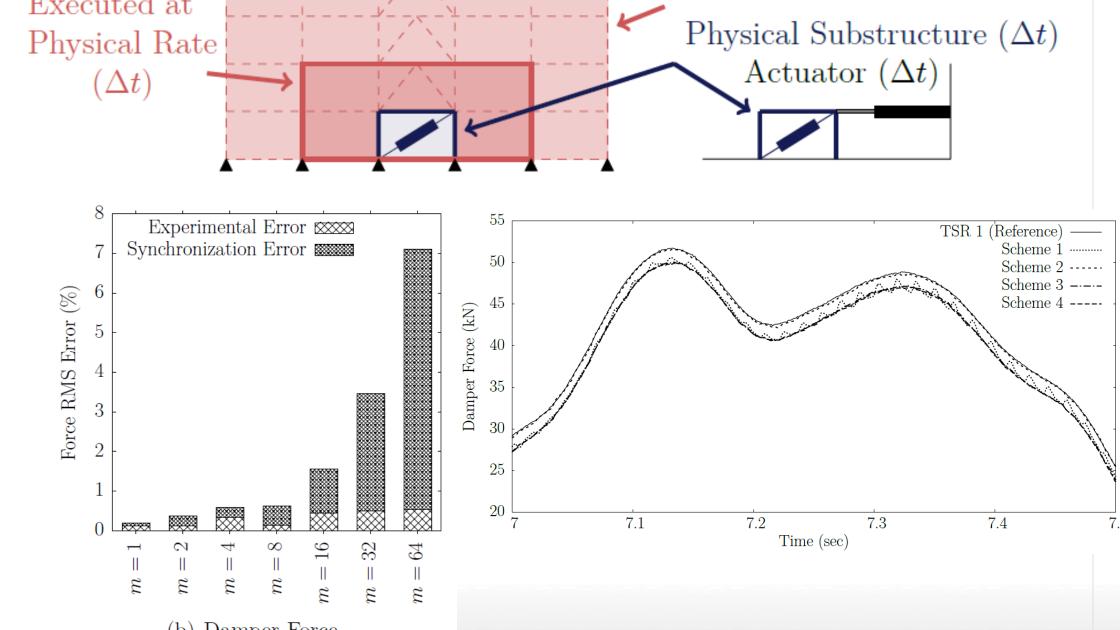


Asynchronous multi-scale models

- > MTS with RTHS is asynchronous
- Need stable predictors for compatibility



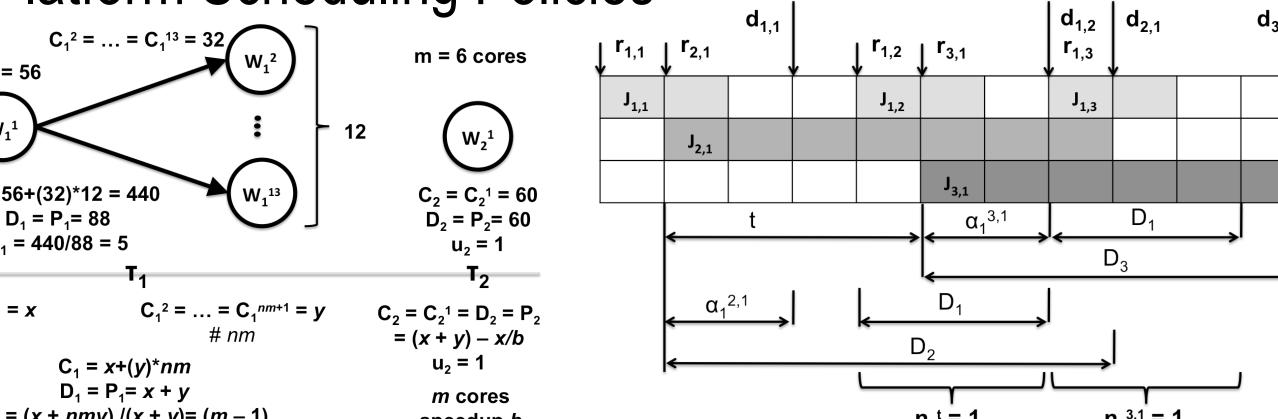


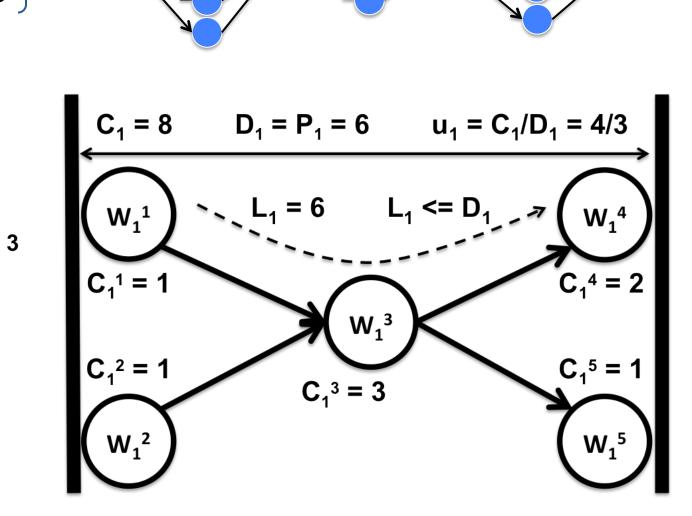


Low Rate Execution (ΔT)

A Concurrency Platform for Cyber-Mechanical Systems

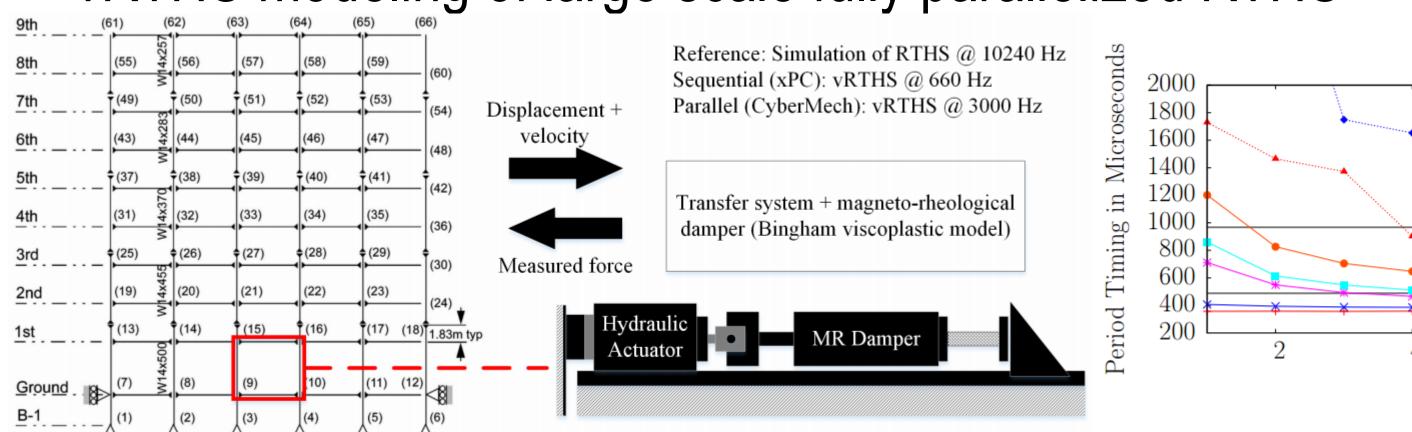
- Cyber-Physical Real-Time Parallel Task Model
 - Platform Scheduling Policies m = 6 cores

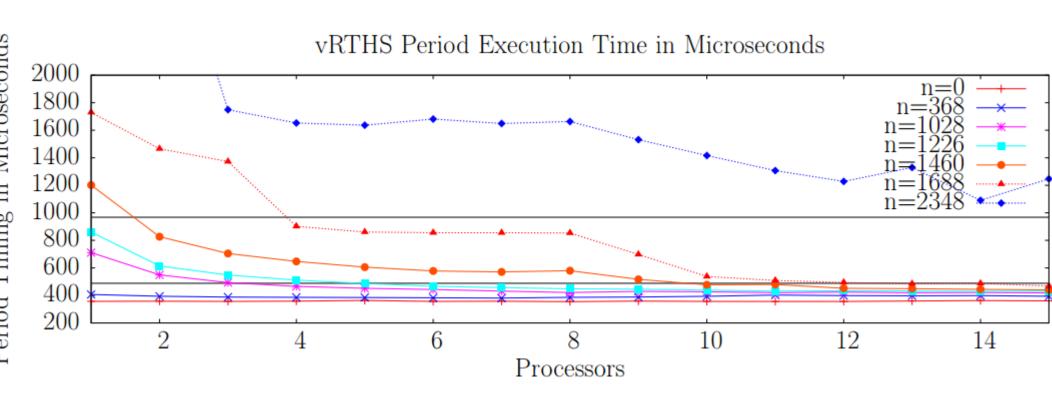




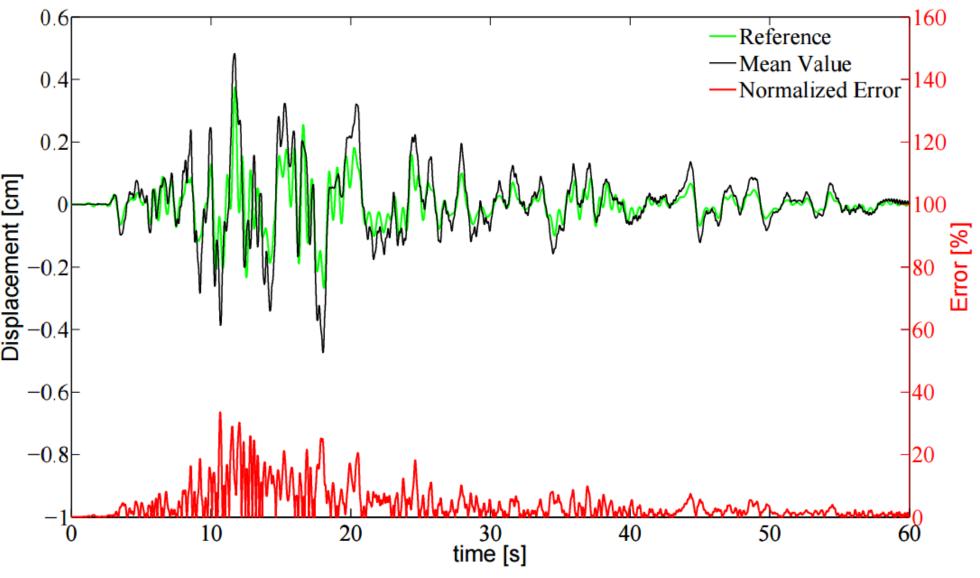
Empirical Evaluation

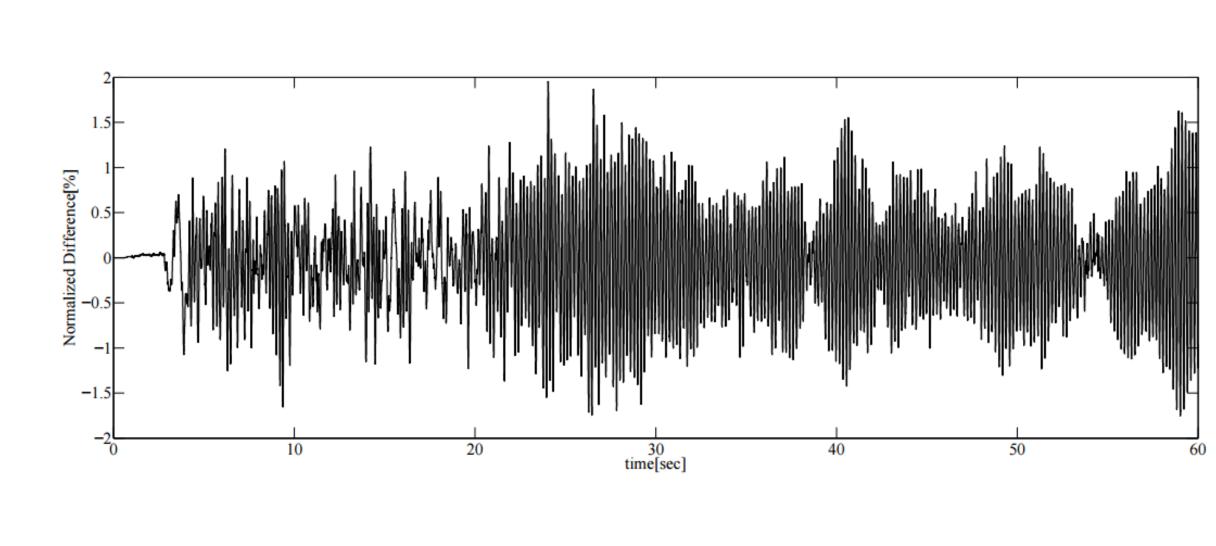
vRTHS modeling of large scale fully parallelized RTHS





Robust RTHS on CyberMech platform – reproducible experimentation





Robust RTHS on CyberMech platform – integrated thread-safe multi-rate behavior

