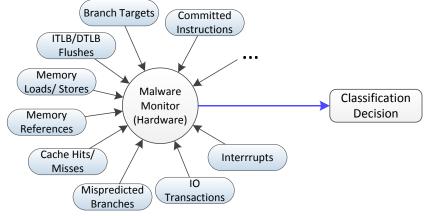
TWC: Small: Practical Hardware-Assisted Always-On Malware Detection UC RIVERSIDE

Challenges:

- How to make hardware malware detection more accurate?
- Can we make hardware malware detectors resilient to evasion?
- How to design a two-level hardwaresoftware detection framework where hardware detector is backed up by a more accurate software detector?



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Scientific Impact:

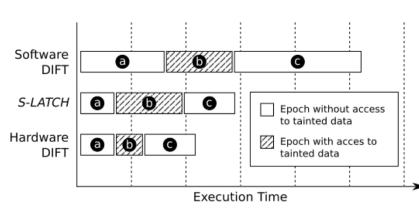
Several papers published in top computer architecture and security conferences and journals (MICRO'17, TDSC'18, ICCAD'18 and MICRO'19)

Key ideas generalize to areas beyond malware detection. For example, MICRO'19 paper shows how to apply twolevel framework to efficient locality-aware DIFT.

Solutions:

- Accurate hardware malware detection based on ensemble of specialized classifiers (TDSC'18, ICCAD'18)
- Design of hardware malware detectors that are resilient to reverse-engineering and evasion (MICRO'17)
- A two-level locality-aware DIFT framework • (MICRO'19).
- A two-level hardware-software framework for • malware detection. An accurate software detector backs up a less accurate, but fast hardware detector (under review).

Project: CNS-1619322 and CNS-1617915. PIs: Nael Abu-Ghazaleh (UCR), Dmitry Ponomarev and Lei Yu (Binghamton University).



Broader Impact:

- The project advanced the understanding of hardware malware detectors and their integration with software ecosystem. This creates opportunities for designing future secure systems in a more efficient and performance-friendly manner.
- Graduate seminar-style course on hardware and systems security has been designed and offered several times at UCR.
 - Several PhD students and undergraduate students have been supported and trained. One of the students (Khaled Khasawneh) became a faculty at GMU in Fall 2019.