# CNS1505701 Security and Privacy-Aware **Cyber-Physical Systems**

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## Platform-Aware Design Framework for Attack-Resilient CPS





- Control-level techniques
  - Attack detection and identification using redundant sensing and model of the system's dynamics
  - Attack-resilient control architectures
- Code-level techniques
  - Ensure that the control code is correctly implemented and integrated
  - Preventing malicious code injection into the controller

**Goal:** Ensure that the system maintains a degree of control even when the system is under *cyber* and/or *physical* attack

#### **Challenges**:

- Existing security-aware control techniques impose very restrictive systems assumptions
  - No noise in the system
  - No DoS attacks
  - Only a subset of sensors can be compromised
- Very conservative requirements on data-integrity

#### Solutions:

- Attack-resilient state estimation in the presence of noise [CDC'15,CSM'17,TCNS'16] - Formal robustness guarantees even for the computationally efficient convex-optimization based estimator
- Control-aware *intermittent* integrity enforcement e.g., using Message [CDC'17,RTSS'17,EMSOFT'17/ACM TECS'17] Authentication Codes (MAC)
  - Case studies: design of resilient automotive features over
    - CAN bus, V2V/I resilient & safe trajectory following with < 20% packets with MAC





## **Integrity Requirements for Resilient Cyber-Physical Systems**

#### Standard Architecture Under Consideration Can we afford security-related overhead?



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## Standard Architecture Under Consideration Idea– Exploit physics to relax security overhead



## Security-Aware Scheduling in CPS Co-design reduces security-related overhead



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# **Thank You**







